

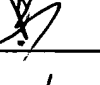

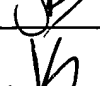
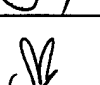
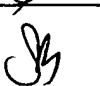





Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)	Attorney Docket No.: 002489P015X2	Application Number: 10/789,299
	First Named Inventor: Jose P. Pereira	
Filing Date: 2/26/04		

OTHER ART - NO PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation ²
		Motorola Semiconductor Technical Data, "Advanced Information 16K X 64 CAM", MCM69C432, January 1996, 4 pages.	
		Advanced Micro Devices, "Final Am99C10A 256 x 48 Content Addressable Memory" Publication No. 08125, Rev. G, December 1992, pp. 1-21.	
		T.V. Lakshman and D. Stiliadis, "High-Speed Policy-Based Packet Forwarding Using Efficient Multi-dimensional Range Matching," <u>SIGCOMM</u> , 1998, pp. 203-214.	
		Xipeng Xiao and Lionel M. Ni, "Internet QoS: A Big Picture," <u>IEEE Network</u> , March/April 1999, pp. 8-18.	
		"White Paper - Introduction to QoS Policies," <u>Stardust.com</u> , 1999, pp. 1-22.	
		"Policy-Based Routing," <u>Cisco Systems, Inc.</u> , 1996, pp. 1-7.	
	✓	Patent Abstracts of Japan, Application No. 09279525, Filed September 26, 1997.	
			
			

Examiner Signature		Date Considered	6/16/06
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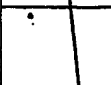
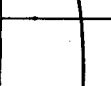
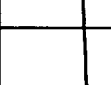
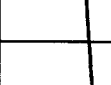
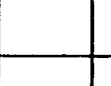
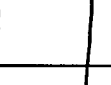
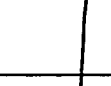

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		Ian N. Robinson, "Pattern Addressable Memory", IEEE Micro, June 1992, pp. 20-30.	
		GEC Plessey Semiconductors, "P2800 2K x 64 Bit Multi-Port Content Addressable Memory", Preliminary Information, February 1997, pp. 1-15.	
		GEC Plessey Semiconductors, "P2800 Multi-port Content Addressable Memory", Functional Product Specification GPS-FPS-2800-12, January 1996.	
	d	Patent Abstracts of Japan, Publication No. 08273376, Publication date 10/18/96.	
		Sergio R. Ramirez-Chavez, "Encoding Don't Cares in Static and Dynamic Content-Addressable Memories", Transaction Briefs, IEEE Transactions on Circuits and System-II: Analog and Digital Signal Processing, Vol. 39, No. 8, August 1992, pp. 575-578.	
		Keikichi Tamaru, "The Trend of Functional Memory Development", INVITED PAPER Special Issue on LSI Memories, IEICE Trans. Electron, Vol. E76-C, No. 11, Nov. 1993, pp. 1545-1554.	
		Hiroshi Kadota et al., "An 8kbit Content-Addressable and Reentrant Memory", IEEE Journal of Solid-State Circuits, Vol. SC-20, No. 5, October 1985, pp. 951-956.	
		Landrock et al., "Associative Storage Module", IBM Technical Disclosure Bulletin, Vol. 25, No. 5, October 1992, pp. 2341-2342.	
		Music Semiconductors, "MU9C1480 LANCAM" Advance Information, March 22, 1990, pp. 1-11.	
		Music Semiconductors, "MUAAT TM CAM Family" Advanced Information, February 24, 1998, Rev. 0, pp. 1-16.	

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		KLSI KE5B064H Series Address Processor Ver. 1.0.1 (believed to be published in late 1995 or early 1996).	
		Kanad Ghose, "Response Pipelined Cam Chips: The First Generation and Beyond," 7 th International Conference on VLSI Design, January 1994, pp. 365-368.	
		Anthony McAuley et al., "A Self-Testing Reconfigurable CAM", IEEE Journal of Solid-State Circuits, Vol. 26, No. 3, March 1991 pp. 257-261.	
		Masao Akata, "A Scheduling Content-Addressable Memory for ATM Space Division Switch Control", IEEE International Solid-State Circuits Conference, Feb. 1991, New York.	
		Sook-ik Chae et al., "Content-Addressable Memory for VLSI Pattern Inspection", IEEE Journal of Solid-State Circuits, Vol. 23, No. 1, February 1988, pp. 74-78.	
		Yong-Chui Shin et al., "A Special-Purpose Content Addressable Memory Chip for Real-Time Image Processing", IEEE Journal of Solid-State Circuits, Vol. 27, No. 5, May 1992, pp. 737-744.	
		Pankaj Gupta et al., "Routing Lookups in Hardware at memory Access Speeds", Proc. Infocom, April 1998, 8 pgs.	
		Masayoshi Kobayashi et al., "A 50Mpps Longest Prefix Match Search Engine LSI for Multi-gigabit IP Forwarding", Technical Report of IEICE IN08-119, November 1998, pp. 7-12.	
		Takeshi Ogura et al., "A 4kbit Associative Memory LSI", IEEE Journal of Solid State circuit, Vol. SC-20, No. 6, December 1985, pp. 1277-1284.	
		Simon R. Jones et al., "A 9-kbit Associative Memory for High Speed Parallel Processing Applications", IEEE Journal of Solid-State Circuits, Vol. 23, No. 2, April 1988 pp. 543-548.	

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		Hayashi, T. et al., "High-Speed Table Lookup Engine for Ipv6 Longest Prefix Match", Global Telecommunications Conference, Vol. 2, page(s) 1576-1581, 1999.	
		Huang et al., "A Novel IP-Routing Lookup Scheme and Hardware Architecture For Multigigabit Switching Routers", IEEE Journal Selected Areas in Communications, pp(s) 1093-1104, 06/1999.	
		Huang et al., "A Fast Routing Lookup Scheme For Gigabit Switching Routers", Eighteenth Annual Joint Conference of the IEEE Computer and Communications Societies, Vol. 3, pp(s) 1429-1436, 1999.	
		High Speed Address Search Engine," Document No. S13450EJ3V0PF00 (3 rd edition), published February 1999, ©NEC Corporation, 2 pages.	
		Pankaj Gupta and Nick McKeown, "Packet Classification on Multiple Fields," <u>SIGCOMM</u> (September 2, 1999), 14 pages.	
		V. Srinivasan, et al., "Packet Classification using Tuple Space Search," <u>SIGCOMM</u> (September 2, 1999), 12 pages.	
		MUSIC Semiconductors, Advance Information, "MUAA Routing CoProcessor (RCP) Family," (October 1, 1998), Rev. 1b, pages 1-16.	
		Music Semiconductors, MU9C2480 LANCAM, Preliminary Data Sheet, August 25, 1995, pp. 1-24.	
		Music Semiconductors, MU9C1480 LANCAM Handbook, November 1994.	
		KLSI KE5B064A1 Address Processor Ver. 2.0.2 Preliminary (believed to be published late 1995 or early 1996).	

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		Waldvogel et al. "Scalable High-Speed Prefix Matching", ACM Transactions on Computer Systems (TOCS), Vol. 19, pages 440-482, Nov. 2001.	
		Srinivasan et al., "Fast Address Lookups Using Controlled Prefix Expansion", ACM Transactions on Computer Systems (TOCS), Vol. 17, pages 1-40, Feb. 1999.	
		Waldvogel et al., "Scalable High Speed IP Routing Lookups", Proceedings of the ACM SIGCOMM '97 Conference on Applications, Technologies, Architectures, and Protocols for Computer Communication, pages 1997.	
		Engineers, Technical Report of IEICE, IN08-119 (1998-11), pages 7-12.	
	✓	Japanese Patent Abstract, Publication No. 04021997A, published January 24, 1992, 1 page.	
	,	Japanese Patent Abstract, Publication No. 05182472A, published July 23, 1993, 1 page.	
		MOSAID, The Next Generation of Content Addressable Memories, MOSAID Technologies Incorporated, September 1999, 8 pages.	
		C.C. Wang and I.H. Horng, "Realization of Bidirection Associative Memory Using a Pseudo-Parallel Searching Approach, 1995 IEEE International Conference on Neural Networks, Vol. 3, pp. 1502-1507, December 1995.	
	Kobayashi, M. et al., "A Processor Based High-Speed Longest Prefix Match Search Engine", IEEE Workshop on High Performance Switching and Routing, page(s) 233-239, 2001.		
		Kobayashi, M. et al. "A Longest Prefix Match Search Engine For Multi-Gigabit IP Processing", IEEE International Conference on Communications, Vol. 3, page(s) 1360-1364, 2000.	

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U.S. PATENT DOCUMENTS						
Exam. Initial*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS								
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U.S. PATENT DOCUMENTS

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		6,181,698		Hariguchi	1/30/01	
		6,237,061		Srinivasan	5/22/01	
		6,266,262		Washburn et al.	7/24/01	
		6,289,414		Feldmeier et al.	9/11/01	
		6,353,873		Melchior	3/5/02	
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		6,065,064		Satoh et al.	5/16/00	
		6,069,573		Clark, II et al.	5/30/00	
		6,081,440		Washburn et al.	6/27/00	
		6,108,227		Voelkel	8/22/00	
		6,115,365		Newberg et al.	9/5/00	
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		5,949,734		Matano	9/7/99	
		5,978,885		Clark, II	11/2/99	
		5,999,435		Henderson et al.	12/7/99	
		6,000,008		Simcoe	12/7/99	
		6,006,306		Haywood, et al.	12/21/99	

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Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)	Attorney Docket No.: 002489P015X2	Application Number: 10/789,299
	First Named Inventor: Jose P. Pereira	
	Filing Date: 2/26/04	

U.S. PATENT DOCUMENTS

Exam. Initial*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Code ²	Kind (If known)			
		5,828,324		Clark, II	10/27/98	
		5,841,874		Kempke et al.	11/24/98	
		5,860,085		Stormon et al.	1/12/99	
		5,870,324		Helwig	2/9/99	
		5,893,931		Peng et al.	4/13/99	
		5,920,886		Feldmeier	7/6/99	
		5,930,359		Kempke et al.	7/27/99	
		5,933,363		Shindo	8/3/99	

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Examiner Signature		Date Considered	2/16/04
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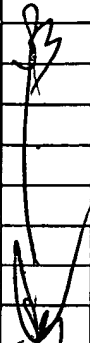
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		5,649,149		Stormon et al.	7/15/97	
		5,699,288		Kim et al.	12/16/97	
		5,706,224		Srinivasan, et al.	1/6/98	
		5,740,098		Adams et al.	4/14/98	
		5,745,488		Thompson et al.	4/28/98	
		5,752,069		Roberts et al.	5/12/98	
		5,818,786		Yoneda	10/6/98	
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		5,592,432		Vishlitzky et al.	1/7/97	
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		5,615,135		Waclawsky et al.	3/25/97	
		5,619,676		Fukuda et al.	4/8/97	
		5,621,677		Jones	4/15/97	
		5,642,114		Komoto	6/24/97	
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







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		5,467,319		Nusinov et al.	11/14/95	
		5,469,161		Bezek	11/21/95	
		5,475,825		Yonezawa et al.	12/12/95	
		5,485,418		Hiraki et al.	1/16/96	
		5,490,102		Jubran	2/6/96	
		5,491,703		Barnaby et al.	2/13/96	
		5,513,134		Cooperman et al.	4/30/96	
		5,517,441		Dietz et al.	5/14/96	

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		Number Code ²	Kind (If known)			
		5,414,704		Spinney	5/9/95	
		5,422,838		Lin	6/6/95	
		5,440,709		Edgar	8/8/95	
		5,440,715		Wyland	8/8/95	
		5,446,685		Holst	8/29/95	
		5,454,094		Montove	9/26/95	
		5,455,576		Clark, II et al.	10/3/95	
		5,465,335		Anderson	11/7/95	

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		Office ³	Number ⁴	Kind Code ⁵ (If known)				

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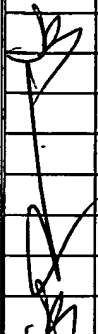
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		Number Code ²	Kind (If known)			
		5,265,100		McClure et al.	11/23/93	
		5,319,590		Montoye	6/7/94	
		5,319,763		Ho et al.	6/7/94	
		5,339,268		Machida	8/16/94	
		5,383,146		Threewitt	1/17/95	
		5,388,189		Kung	2/7/95	
		5,396,449		Atallah et al.	3/7/95	
		5,406,508		Hayashibara	4/11/95	

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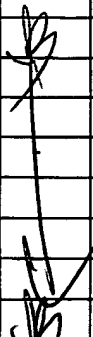
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		Number Code ²	Kind (If known)			
		5,036,486		Noguchi et al.	7/30/91	
		5,051,948		Watabe et al.	9/24/91	
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		5,072,422		Rachels	12/10/91	
		5,107,501		Zorian	4/21/92	
		5,111,427		Kobayashi et al.	5/5/92	
		5,226,005		Lee et al.	7/6/93	
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		4,903,234		Sakaraba et al.	2/20/90	
		4,928,260		Chuang et al.	5/22/90	
		4,958,377		Takahashi	9/18/90	
		4,959,811		Szezepanek	9/25/90	
		4,975,873		Nakabayashi et al.	12/4/90	
		4,996,666		Duluk, Jr.	2/26/91	
		5,010,516		Oates	4/23/91	
		5,014,195		Farrell et al.	5/7/91	

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		4,656,626		Yudichak et al.	4/7/87	
		4,670,858		Almy	6/2/87	
		4,747,080		Yamada	5/24/88	
		4,758,982		Price	7/19/88	
		4,780,845		Threewitt	10/25/88	
		4,785,398		Joyce et al.	11/15/88	
		4,791,606		Threewitt et al.	12/13/88	
		4,813,002		Joyce et al.	3/14/89	
		4,845,668		Sano et al.	7/4/89	

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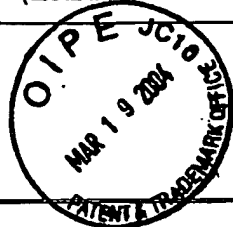
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		3,602,899		Lindquist et al.	8/31/71	
		3,675,211		Raviv	7/4/72	
		3,685,020		Meade	8/15/72	
		3,868,642		Sachs	2/25/75	
		4,112,502		Scheuneman	9/5/78	
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Exam. Initial	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (If known)				


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	Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)	Attorney Docket No.: 002489P015X2	Application Number: 10/789,299
		First Named Inventor: Jose P. Pereira	
		Filing Date: 2/26/04	

U.S. PATENT DOCUMENTS						
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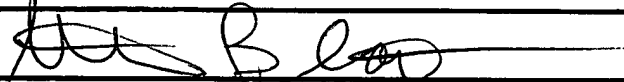

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		Number Code ²	Kind (If known)			
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
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OTHER ART - NO PATENT LITERATURE DOCUMENTS

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		Masayoshi Kobayashi et al. "A 50 Mpps Longest Prefix Match Search Engine IC for Multi-gigabit IP Forwarding", The Institute of Electronics Information and Communication, 15 pages, 1998.	
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